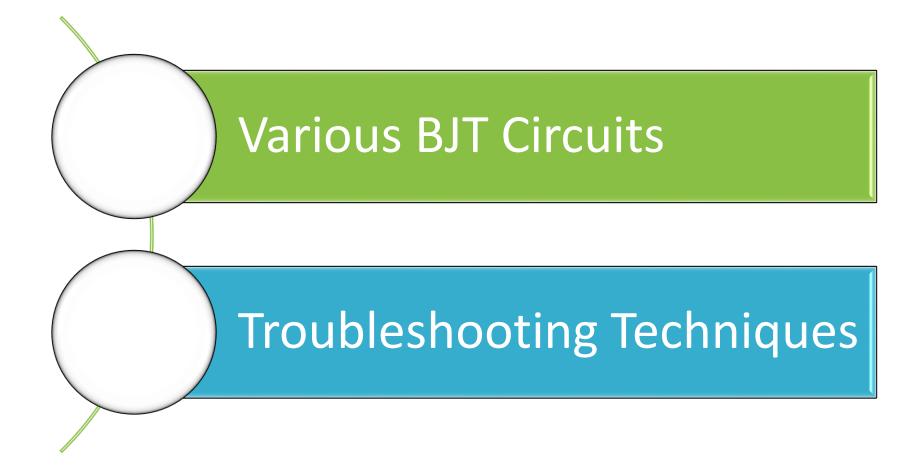
ECE 121 Electronics (1) Lec. 3: BJT Circuits & Troubleshooting Instructor Dr. Maher Abdelrasoul

http://www.bu.edu.eg/staff/mahersalem3

# Outline

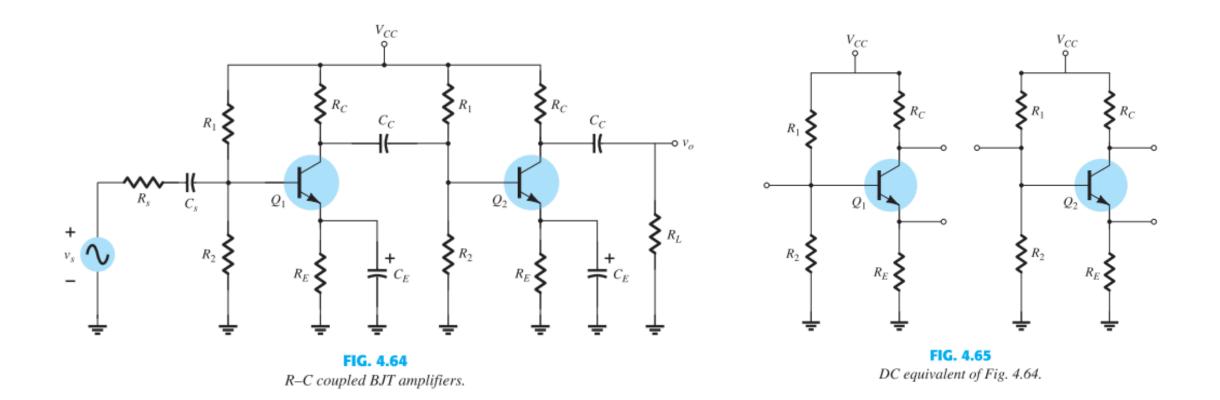


# Various BJT Circuits

- MULTIPLE BJT NETWORKS
- CURRENT MIRRORS
- CURRENT SOURCE CIRCUITS
  - Bipolar Transistor Constant-Current Source
  - Transistor/Zener Constant-Current Source
- PNP TRANSISTORS
- TRANSISTOR SWITCHING NETWORKS

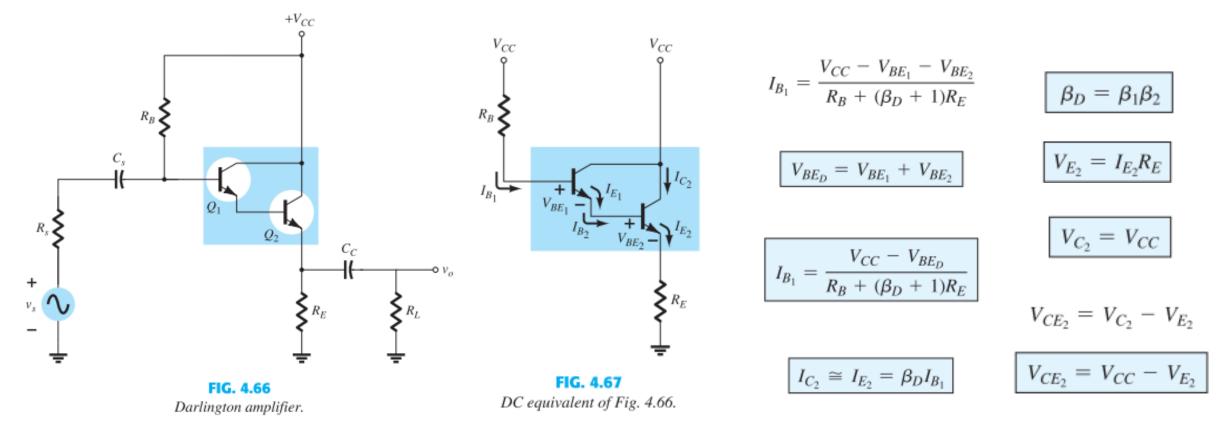
# Multiple BJT Networks (1 of 6)

• R–C coupling



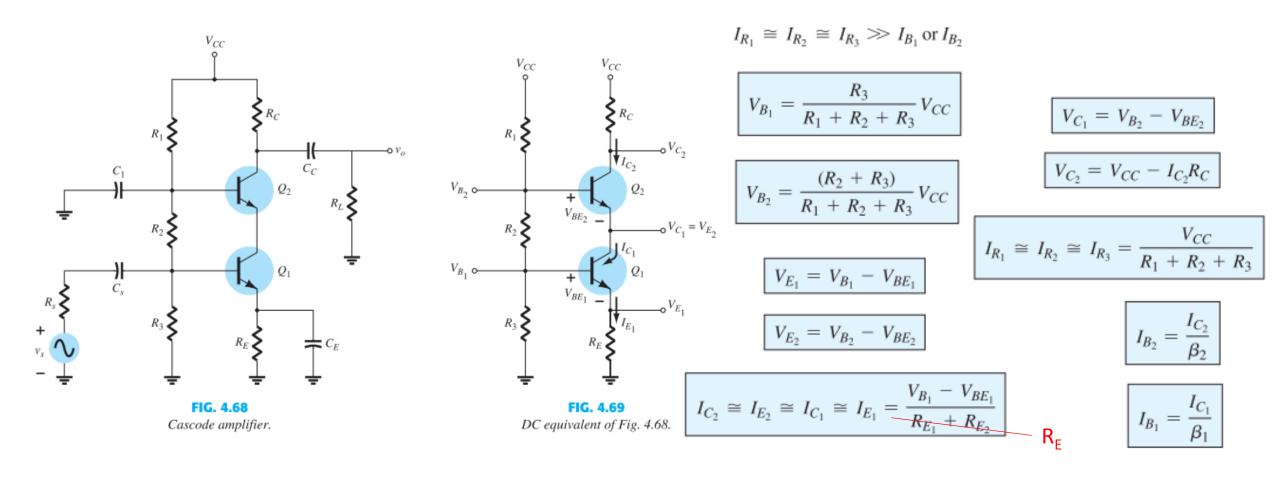
# Multiple BJT Networks (2 of 6)

• Darlington configuration



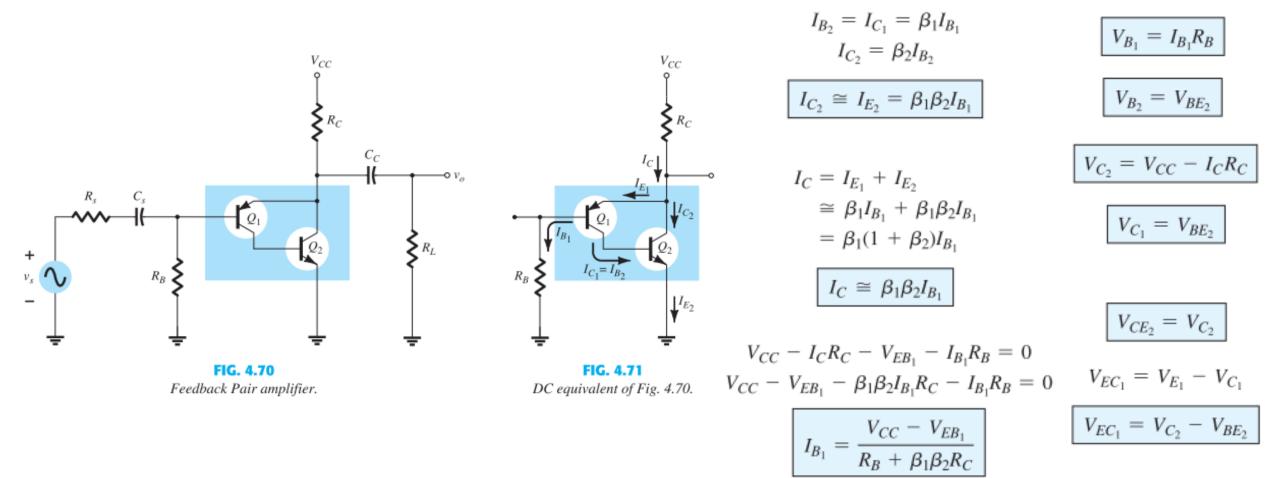
# Multiple BJT Networks (3 of 6)

Cascode configuration



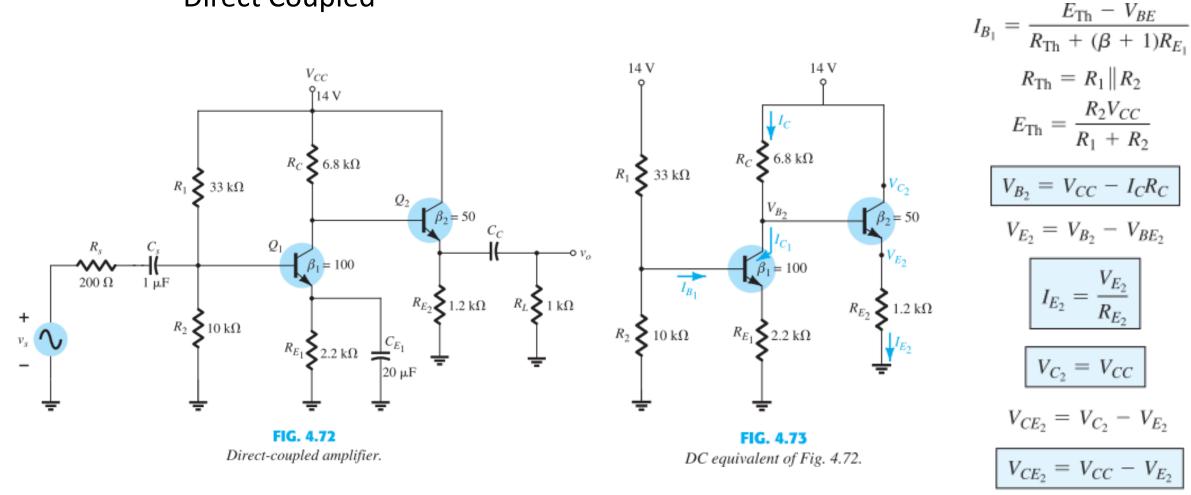
# Multiple BJT Networks (4 of 6)

• Feedback Pair



# Multiple BJT Networks (5 of 6)

• Direct Coupled



# Multiple BJT Networks (6 of 6)

 $R_{\rm Th} = 33 \, {\rm k}\Omega \parallel 10 \, {\rm k}\Omega = 7.67 \, {\rm k}\Omega$ 

In this case,

and

so that

with

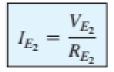
$$E_{\rm Th} = \frac{10 \,\mathrm{k}\Omega(14 \,\mathrm{V})}{10 \,\mathrm{k}\Omega + 33 \,\mathrm{k}\Omega} = 3.26 \,\mathrm{V}$$
$$I_{B_1} = \frac{3.26 \,\mathrm{V} - 0.7 \,\mathrm{V}}{7.67 \,\mathrm{k}\Omega + (100 + 1) \,2.2 \,\mathrm{k}\Omega}$$
$$= \frac{2.56 \,\mathrm{V}}{229.2 \,\mathrm{k}\Omega}$$
$$= 11.17 \,\mu\mathrm{A}$$
$$I_{C_1} = \beta I_{B_1}$$
$$= 100 \,(11.17 \,\mu\mathrm{A})$$
$$= 1.12 \,\mathrm{m}\mathrm{A}$$

In Fig. 4.73 we find that

$$V_{B_2} = V_{CC} - I_C R_C$$
  
= 14 V - (1.12 mA)(6.8 kΩ)  
= 14 V - 7.62 V  
= 6.38 V

and

 $V_{E_2} = V_{B_2} - V_{BE_2}$ = 6.38 V - 0.7 V = 5.68 V



	5.68 V
	$1.2 \text{ k}\Omega$
=	4.73 mA

Obviously,

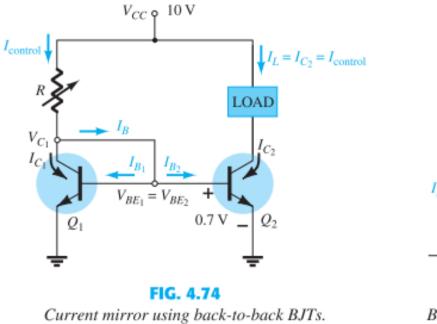
and

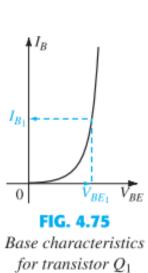
resulting in

$$V_{C_2} = V_{CC}$$
$$= 14 \text{ V}$$
$$V_{CE_2} = V_{C_2} - V_{E_2}$$

 $V_{CE_2} = V_{CC} - V_{E_2}$ = 14 V - 5.68 V = 8.32 V

# Current Mirrors (1 of 2)





(and  $Q_2$ ).

$$I_{\rm control} = \frac{V_{CC} - V_{BE}}{R}$$

$$I_{\text{control}} = I_{C_1} + I_B = I_{C_1} + 2I_{B_1}$$
$$I_{C_1} = \beta_1 I_{B_1}$$
$$I_{\text{control}} = \beta_1 I_{B_1} + 2I_{B_1} = (\beta_1 + 2)I_{B_1}$$

 $\beta_1$  is typically  $\gg 2$ ,  $I_{\text{control}} \cong \beta_1 I_{B_1}$ 

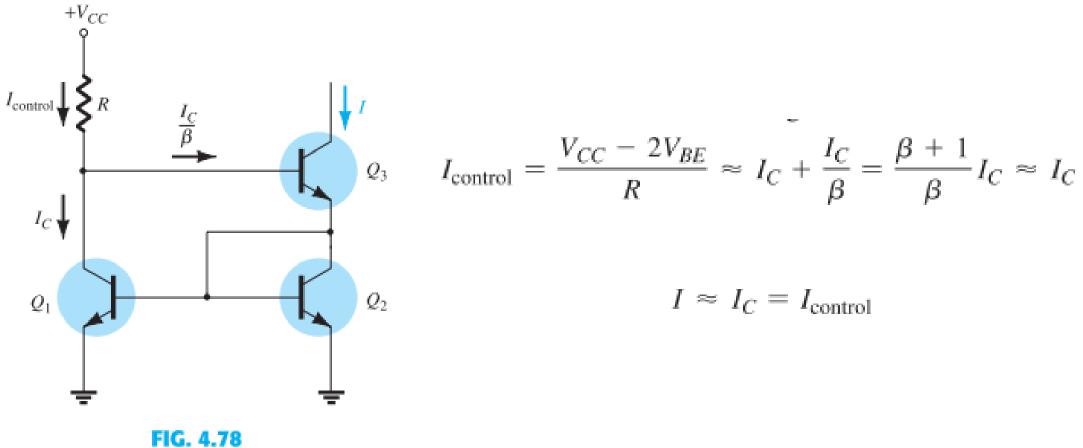
$$I_{B_1} = \frac{I_{\text{control}}}{\beta_1}$$

$$I_L = I_{C_2} = \beta \bar{I}_{B_2}$$

$$I_{L} \uparrow I_{C_{2}} \uparrow I_{B_{2}} \uparrow V_{BE_{2}} \uparrow V_{CE_{1}} \uparrow, I_{R} \downarrow, I_{B} \downarrow, I_{B_{2}} \downarrow I_{C_{2}} \downarrow I_{L} \downarrow$$

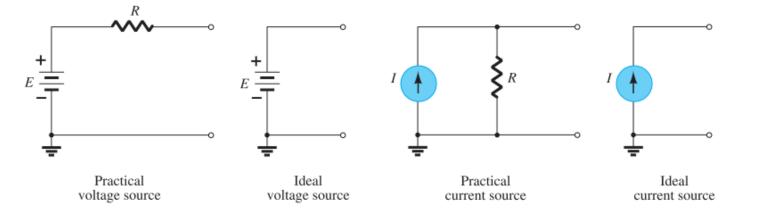
$$Note$$

# Current Mirrors (2 of 2)

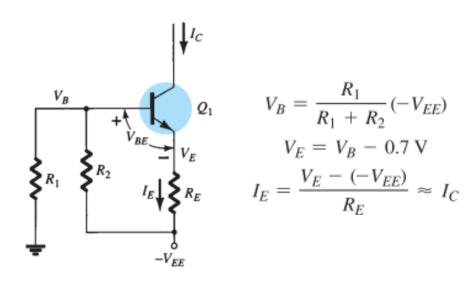


Current mirror circuit with higher output impedance.

### Current Source Circuits (1 of 2)



#### **Bipolar Transistor Constant-Current Source**



#### Transistor/Zener Constant-Current Source

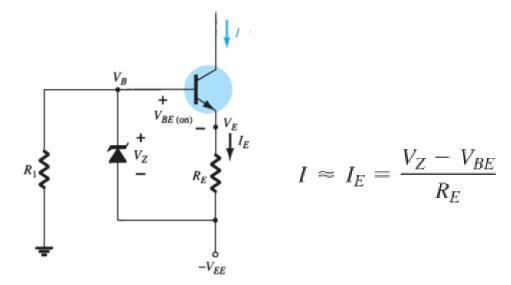
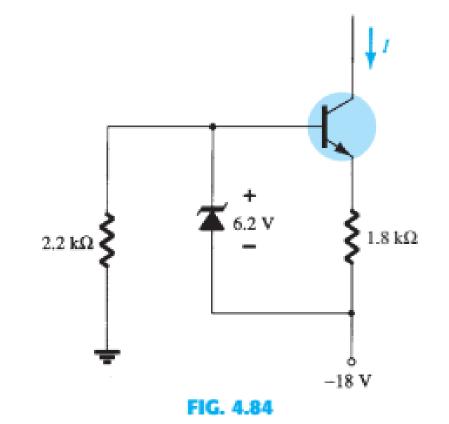


FIG. 4.83 Constant-current circuit using Zener diode.

FIG. 4.81 Discrete constant-current source.

### Current Source Circuits (2 of 2)

**EXAMPLE 4.30** Calculate the constant current *I* in the circuit of Fig. 4.84.



Constant-current circuit for Example 4.30.

Solution:

Eq. (4.83): 
$$I = \frac{V_Z - V_{BE}}{R_E} = \frac{6.2 \text{ V} - 0.7 \text{ V}}{1.8 \text{ k}\Omega} = 3.06 \text{ mA} \approx 3 \text{ mA}$$

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# PNP Transistors (1 of 2)

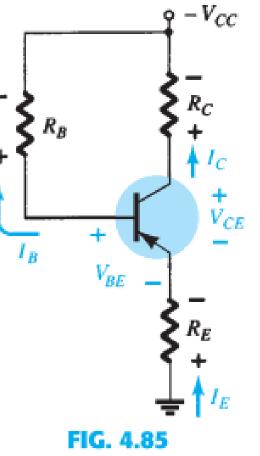
- The analysis thus far has been limited totally to *npn* transistors.
- Fortunately, the analysis of *pnp* transistors follows the same pattern established for *npn* transistors.
- In fact, the only difference between the resulting equations for a network in which an *npn* transistor has been replaced by a *pnp* transistor is the sign associated with particular quantities.

$$-I_E R_E + V_{BE} - I_B R_B + V_{CC} = 0$$

$$I_B = \frac{V_{CC} + V_{BE}}{R_B + (\beta + 1)R_E}$$

$$-I_E R_E + V_{CE} - I_C R_C + V_{CC} = 0$$

$$V_{CE} = -V_{CC} + I_C(R_C + R_E)$$



pnp transistor in an emitterstabilized configuration.

# PNP Transistors (2 of 2)

**EXAMPLE 4.31** Determine  $V_{CE}$  for the voltage-divider bias configuration of Fig. 4.86.

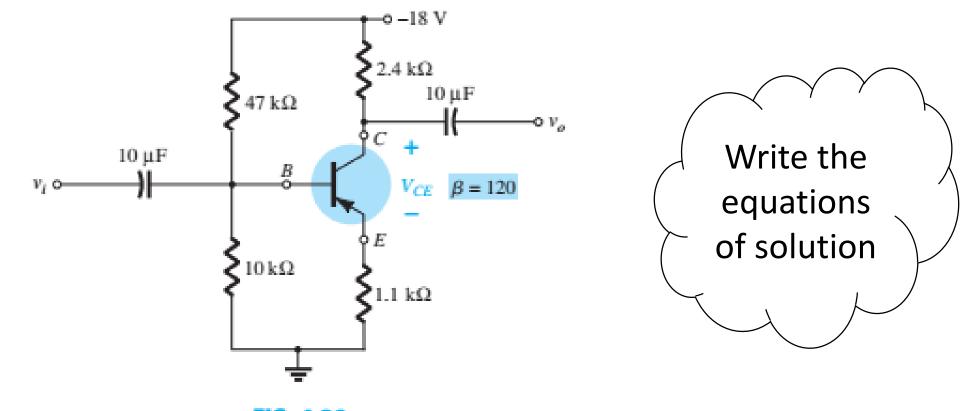
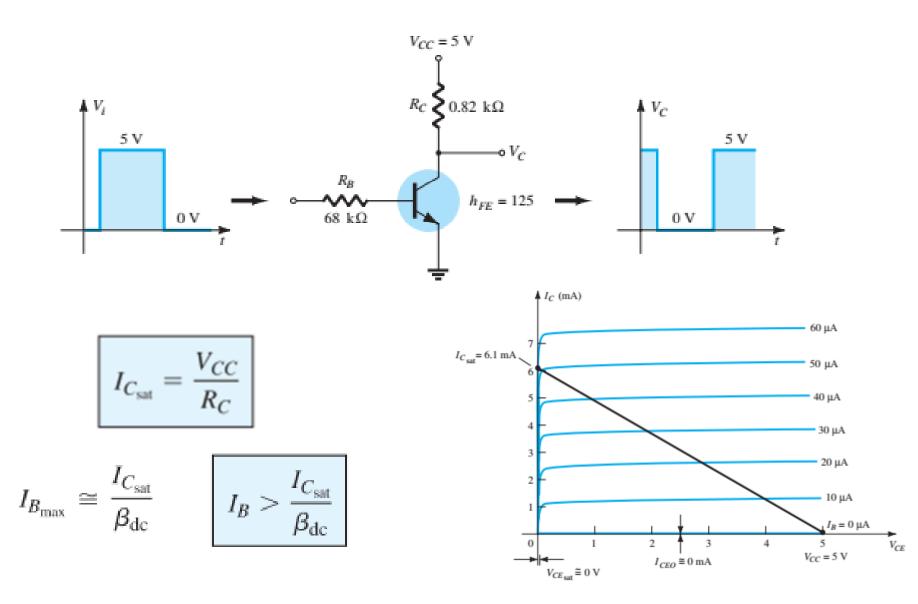


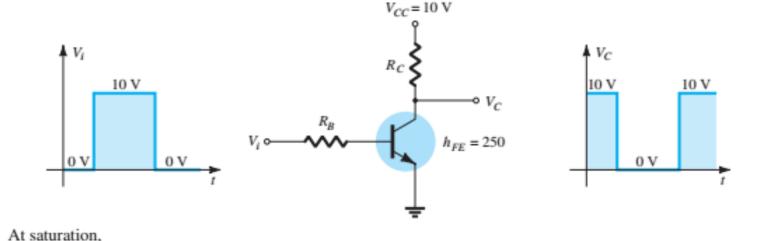
FIG. 4.86 pnp transistor in a voltage-divider bias configuration.

# Transistor Switching Networks (1 of 3)



### Transistor Switching Networks (2 of 3)

**EXAMPLE 4.32** Determine  $R_B$  and  $R_C$  for the transistor inverter of Fig. 4.90 if  $I_{C_{sat}} = 10$  mA.



Solution: At saturation

and

so that

At saturation,

$$I_B \cong \frac{I_{C_{\text{sat}}}}{\beta_{\text{dc}}} = \frac{10 \text{ mA}}{250} = 40 \,\mu\text{A}$$

 $I_{C_{\text{sat}}} = \frac{V_{CC}}{R_C}$ 

 $10 \text{ mA} = \frac{10 \text{ V}}{R_C}$ 

 $R_C = \frac{10 \text{ V}}{10 \text{ mA}} = 1 \text{ k}\Omega$ 

Choosing  $I_B = 60 \,\mu\text{A}$  to ensure saturation and using

$$I_B = \frac{V_i - 0.7 \,\mathrm{V}}{R_B}$$

we obtain

$$R_B = \frac{V_i - 0.7 \,\mathrm{V}}{I_B} = \frac{10 \,\mathrm{V} - 0.7 \,\mathrm{V}}{60 \,\mu\mathrm{A}} = 155 \,\mathrm{k}\Omega$$

Choose  $R_B = 150 \text{ k}\Omega$ , which is a standard value. Then

$$I_B = \frac{V_i - 0.7 \,\mathrm{V}}{R_B} = \frac{10 \,\mathrm{V} - 0.7 \,\mathrm{V}}{150 \,\mathrm{k}\Omega} = 62 \,\mu\mathrm{A}$$

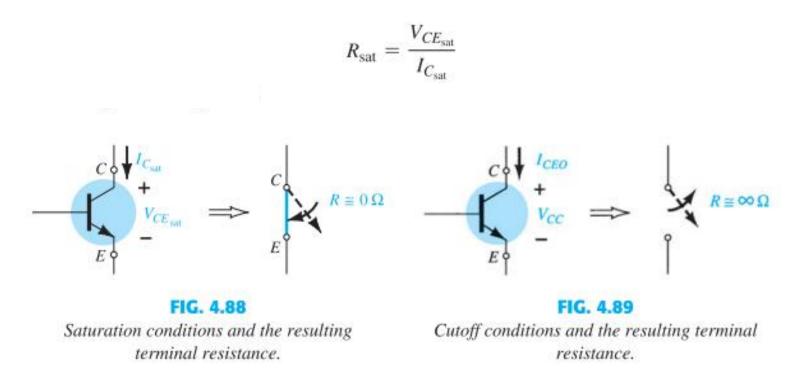
and

 $I_B = 62 \,\mu\text{A} > \frac{I_{C_{\text{sat}}}}{\beta_{\text{dc}}} = 40 \,\mu\text{A}$ 

Therefore, use  $R_B = 150 \,\mathrm{k}\Omega$  and  $R_C = 1 \,\mathrm{k}\Omega$ .

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#### Transistor Switching Networks (3 of 3)



Using a typical average value of  $V_{CE_{sat}}$  such as 0.15 V gives

$$R_{\text{sat}} = \frac{V_{CE_{\text{sat}}}}{I_{C_{\text{sat}}}} = \frac{0.15 \text{ V}}{6.1 \text{ mA}} = 24.6 \Omega$$
$$R_{\text{cutoff}} = \frac{V_{CC}}{I_{CEO}} = \frac{5 \text{ V}}{0 \text{ mA}} = \infty \Omega$$
$$R_{\text{cutoff}} = \frac{V_{CC}}{I_{CEO}} = \frac{5 \text{ V}}{10 \mu \text{ A}} = 500 \text{ k}\Omega$$

## Troubleshooting Techniques

- For an "on" transistor, the voltage  $V_{BE}$  should be in the neighborhood of 0.7 V.
- For the typical transistor amplifier in the active region,  $\rm V_{CE}~$  is usually about 25% to 75% of  $\rm V_{CC}$  .

